



PATENT
Atty. Docket: 1400-35 (1568)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Jeffrey J. Terlizzi et al. Examiner: Hess, Daniel A.
Serial No. : 10/650,182 Group Art Unit: 2876
Filed : August 28, 2003
For : **MULTI-INTERFACE DATA ACQUISITION
SYSTEM AND METHOD THEREOF**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA. 22313-1450

DECLARATION UNDER CFR 37 1.131

Dear Madam:

I, Tong-Hsaio Chang, declare and say that:

I am the main author of attached Exhibit A and Exhibit B.

The attached Exhibits A and B illustrate a multi-interface controller having the elements claimed in Applicants' patent application entitled, "MULTI-INTERFACE DATA ACQUISITION SYSTEM AND METHOD THEREOF", filed on August 28, 2003 and having Serial No.10/650,182.

Exhibit A is dated December 3, 1999 and Exhibit B is dated December 8, 1999.

The dates December 3, 1999 and December 8, 1999 both antedate the priority date of U.S. Patent No. 6,705,527 by Kelly et al., filed on June 1, 2001 and claiming the priority date June 1, 2000, and cited as a prior art reference by the Examiner in the Office Action of December 19, 2005.

The attached Exhibits A and B demonstrate that Applicants appreciated the subject matter recited by the claims of the above-identified patent application before the priority date of June 1, 2000 of Patent No. 6,705,527.

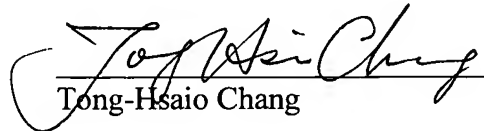
The undersigned declares further that all statements made herein of his own knowledge are true and that all statements made on information and belief are

believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issuing thereon.

Respectfully submitted,

5/18/06

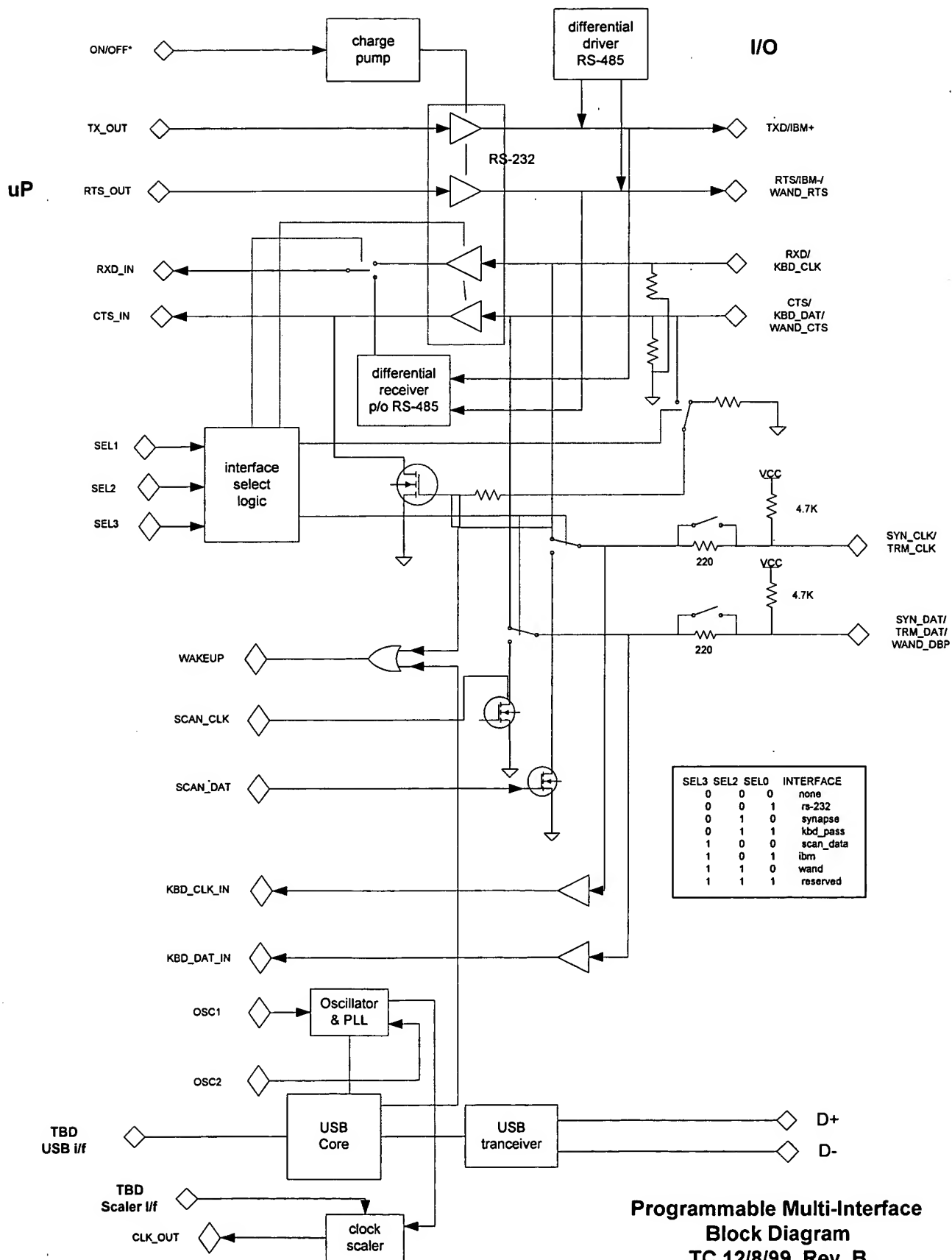
Dated



Tong-Hsaio Chang

EXHIBIT A

EXHIBIT B



SEL3	SEL2	SEL0	INTERFACE
0	0	0	none
0	0	1	rs-232
0	1	0	synapse
0	1	1	kbd_pass
1	0	0	scan_data
1	0	1	ibm
1	1	0	wand
1	1	1	reserved

Programmable Multi-Interface
Block Diagram
 TC 12/8/99 Rev. B
 Proprietary Information